## 3.3 Software design

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### 1 Introduction

The MagAO project was constructed around a working AO system, that of the LBT. We re-used the real-time control software as-is, and adapted the AO control software developed by Arcetri (hereafter the AdOpt system) to the Magellan computing environment. In addition, we developed a major extension to the LBTAO baseline by adding the VisAO camera which works seamlessly as part of the MagAO system and routinely and reliably takes science data on the 6.5 m Clay telescope. We also developed an interface for the Clio camera to work as part of MagAO without rewriting the Clio control software itself.

The overall philosophy of MagAO-X software development will closely follow that used for MagAO: we will base it on a working AO control architecture (in this case SCExAO) and adapt it for our use, minimizing truly new software development. We will save significant development time through our use of the same components which are already in use at SCExAO or on the existing MagAO system. These components include:

1. The BMC 2k Deformable Mirror
2. The OCAM-2k EMCCD PWFS detector
3. The PI TTM head
4. Filter wheel motors
5. Tip/tilt stage actuators
6. PI stages

In Section 3.2 we presented the preliminary design of the MagAO-X compute system. This includes the real-time computer (RTC), the instrument control computer (ICC), and the AO operations computer (AOC), as well as workstations in the Clay control room. Here we describe the software preliminary design.

### 2 Software Management

#### 2.1 Version Control:

`git` will be used for version control, with a repository hosted on `github`. The standard "centralized workflow" will be used, where development occurs on local copies, with changes committed to the central repository.

The `git` SHA-1 hash (essentially the version number) will be used as a reproducibility tracer. The SHA-1 of the `git` repo at the time data or calibrations are taken will be traceable, either via timestamps or (when appropriate) by writing the SHA-1 to metadata. To facilitate this, all processes will have the SHA-1 embedded at compile time
and will record this in their log at startup\(^1\). This ensures that at any time the state of the software system can be recovered if needed to understand data recorded in the past.

Configuration files will also be kept under version control with `git`, and similarly the SHA-1 will be tracked and traceable.

Note: for this system to work, it will be policy that in general no data will be taken with uncommitted changes in the local repository. The user interface will warn when this is occurring (including compiler warnings). This will be strictly enforced on the telescope. Common sense will be allowed during development and lab testing\(^2\).

### 2.2 Coding Standards and Documentation:

The main language used for MagAO-X development will be C++. This is mainly driven by performance, and the PI/Software-lead is proficient in modern C++. The SCExAO real-time code is written in C, so adaptation of that code base for our use will be straightforward. The AdOpt low-level code is also primarily in C, so re-use of various motion control code will be straightforward. The INDI library is also provided in C++.

Python will also be used for scripting and other tasks.

All new code will be documented for processing with doxygen. Doxygen is a well known and maintained code-documentation system. It allows for programmers to document code as they go, with the addition of a few markup symbols. The result is nicely formatted HTML documentation, with browseable source code, indices, etc, all automatically generated from source. We will also use this to document application interfaces (command line options and configuration file parameters). The VisAO camera control software demonstrates this, [https://visao.as.arizona.edu/software_files/visao/html/annotated.html](https://visao.as.arizona.edu/software_files/visao/html/annotated.html), though we expect to improve on the application interface documentation significantly over what is shown there.

A minimum coding standard will be adhered to, which defines such things as header layouts, declare/define standards, documentation conventions, etc. We provide a draft version of this in Appendix A.

### 3 Computer Design

The MagAO-X computing system includes three custom computers: the Real-Time Computer (RTC), the Instrument Control Computer (ICC), AO Operations Computer (AOC). The specifications and mechanical design of these three computers is presented in Section 3.2. MagAO-X will also make use of the existing workstations in the Magellan Clay control room for science operations (zorro and guanaco).

#### 3.1 Operating System:

MagAO-X will standardize on 64-bit CentOS 7, chosen for long term stability. The expected lifetime for CentOS 7 is\(^3\):

- Full Updates: through the end of 2020
- Maintenance Updates: through 2024-June-30.

This will ensure a stable computing environment throughout the development, commissioning, and first four operating years of the instrument.

\(^1\)We already use this technique in data reduction, see this script which creates a header to accomplish it: [https://bitbucket.org/jaredmales/mxlib/src/6aec98c12c7fddd02bce3b78c58402e9ab62cb0/gengithead.sh?at=master&fileviewer=file-view-default](https://bitbucket.org/jaredmales/mxlib/src/6aec98c12c7fddd02bce3b78c58402e9ab62cb0/gengithead.sh?at=master&fileviewer=file-view-default)

\(^2\)SHA-1s are free

\(^3\)see [https://wiki.centos.org/About/Product](https://wiki.centos.org/About/Product)
CentOS 7 also has the advantage that up-to-date real-time (RT) kernel packages are readily available from the CERN repositories. The RT kernel is used in the existing MagAO system on the VisAO computer, where priorities were optimized for low-latency in several critical processes. The RT kernel will be employed on the RTC and the ICC, and the AOC if needed.

### 4 Instrument Control

Here we describe our preliminary design for the instrument control software system (ICSS). This encompasses control of the various stages and motors, the science cameras, and high level AO loop control (stop/start, status monitoring etc).

#### 4.1 INDI:

We will employ the Instrument Neutral Distributed Interface (INDI) protocol for communication between the various components of the ICSS. INDI is now the de-facto standard within the Center for Astronomical Adaptive Optics (CAAO), where it is used for the LBTI control software and is an integral part of the planned MMTAO upgrade. Using it has the advantage that one of the main developers of INDI is a member of CAAO making support readily available.

INDI essentially replaces the real-time-database (RTDB) and message daemon (MsgD) middle-ware in the AdOpt architecture. The basic architecture is that INDI devices communicate with a simple protocol via an INDI server on the host machine, see Figure 1 left. INDI servers are connected over the network, providing communications between machines. A very nice CGI interface is possible, which will provide a light-weight interface for astronomers to use, see Figure 1 right.

![Figure 1: The INDI architecture. Source: the INDI wiki.](http://linux.web.cern.ch/linux/rt/)

For MagAO-X, each of the RTC, ICC, and AOC will have an INDI server running and communicating with the others. On RTC INDI will provide for monitoring the status of the AO loop, high level AO control (start/pause/stop, etc), and show component status (PWFS camera, DMs). On the ICC INDI will be used to control the various stages, motors, and mechanisms, control and interact with the science cameras, and monitor the status of the LOWFS loop. On the AOC, an INDI device will interface to the TCS. The AOC INDI server will support the AO Operations Interface, and through the fast-CGI capability and a web server provide the astronomer’s interface. The MagAO-X INDI architecture is shown in Figure 2.

[http://linux.web.cern.ch/linux/rt/]
4.2 MagAOXApp: The class defining an application in the MagAO-X ICSS will be derived from a standard class called MagAOXApp. Similar to the AOApp and VisAOApp base class in the AdOpt and VisAO systems, this will provide a standard set of functionality. This will include the INDI driver and client facilities, configuration, logging, and management of real-time priority.

4.3 Configuration: For MagAO-X we will use ini-style configuration files. This a standard format using key=value pairs and allows sections. For example

```
[basic]
name=The Name
rt_priority=0

[section2]
avector=0,3,5,6,3
```

Each derived class is responsible for knowing the intended type of each value. A template-based configuration parsing system will be used for ease of coding.

The MagAOXApp will employ a cascaded configuration system. At startup, the application will configure itself using the following sources in order:

1. Default configuration [compiled in]
2. System Global configuration [set by environment variable, common to all MagAOXApp processes]
3. App global configuration [location set by environment variable, name compiled in]
Only the default configuration needs to exist. Each level overrides the previous. By specifying the location of configuration files via environment variables, we will have a straightforward way to maintain several configurations (e.g. for lab, cleanroom, and telescope).

### 4.4 Interprocess Communication:
Non-real-time interprocess communication will generally take place via the INDI protocol. Any process which needs the status of another will subscribe to the appropriate property. An example is a focus stage which may need to know the position of several filter wheels in order to go to the correct position. This will also suffice for such things as AO setup (i.e. which reconstructor to load could depend on beamsplitter selection, among other things).

Real-time IPC on the ICC will make use of shared memory and semaphores. For instance, the science camera controllers will notify the framegrabber process for that camera when the format has changed via a semaphore, cueing the framegrabber process to read the shared memory buffer containing the configuration details. The framegrabber in turn notifies the frame-writer process every time a new image is ready to write to disk.

The main AO control real-time software is described below.

### 4.5 Logging:
Event logging is a crucial facility for a system such as MagAO-X. Here we include recording specific events (“loop closed”) as well as telemetry such as WFS images and telescope position. These data will be used for system performance analysis and diagnosis, and perhaps more importantly for data reduction. Given our goal of recording all system data for future use in data reduction, we want to have a very efficient log system.

A lesson learned from the AdOpt system is that ASCII logs can use a great deal of disk space over time — especially when things going wrong causes frequent logging, the time when we least want to be managing disk space. Furthermore, having a somewhat rigid log structure should be more efficient for later analysis. In MagAO-X we will address these issues by logging only an an event code and a time-stamp, along with data of known format based on the event code, all in binary. That is log files will not be easily human-readable as stored on disk.

The event code is a 32-bit fixed width unsigned integer, `uint32_t`. This gives 4,294,967,296 independent event codes, which we assess is more than enough.

The time-stamp will be stored as two fixed-width integers, where the first `int64_t` holds the whole seconds since the Unix epoch and the second `int32_t` holds the nanosecond. This is the `timespec` structure, except we are explicit about the integer width (another lesson learned from AdOpt, where the `timeval` structure was a source of 32/64 compatibility problems, though we are unlikely to use 32-bit systems).

So a time stamp consists of a minimum of 12 bytes. For comparison logging to nanosecond precision with ASCII requires a string of the form YYYYMMDDHHMMSSNNNNNNNNN, which is 23 bytes. This could be hex encoded, say, reducing it to 12 bytes. We could use a 32-bit unsigned integer for the seconds field, but this would reduce compatibility with the standard library on 64-bit linux. Consider, though, that MagAO-X has a maximum operating frequency of 3700 Hz. Assume that an average of 10 events are logged each timestep (a conservatively high number). The extra 4 bytes then amounts to 3700*10*4 = 16 kB/sec = 5 GB/10-hrs. This is a relatively small overhead compared to the several TB of image data we will record in the same time and so we consider it negligible.

Human readable logs in ASCII would take the form:

```
YYYY-MM-DDTHH:MM:SS.SSSSSSSSS INF loop closed\n [ 46 bytes ]
```

And place us at risk of the Unix millenium bug in 2038, or 2106 if we used unsigned integers. We may not operate for that long though, so this if of minor concern.
In contrast, these three log entries will take 16, 16, and 24 bytes respectively, or 37% of the disk space.

Logs will then be saved as binary records in HDF5 files on a per-process basis. For efficient access, these records will be have a maximum length (to be optimized) and have the timestamp of the first entry recorded as an attribute in the file. Parsing each record will require determining the type from the event code and calling an appropriate function (with a pointer to the entry as argument) to read the data.

Simple utilities will display logs in human-readable format as needed (i.e. a replacement for tools like cat).

Using c++ templates we will provide a very simple logging interface within the code. A sketch of how this will work is shown in Appendix B.

A drawback to this system will be the overhead of creating a new event code. This overhead will be paid during development, every time a new log entry is needed. The minimum steps to create a new log entry will include:

1. declare log entry structure containing the event code
2. define length() member of the struct
3. declare and define a specialized log<> () function to do the work of logging.

In general this will also necessitate updating the log parsing facility to handle this new event type. A database of event codes will also be maintained automatically with a code analyzer minimizing effort to safely generate a new one.

4.6 RT Priority: MagAOXApp based processes will have the ability to set their real-time (RT) priority. This will be determined by a configuration setting, allowing for optimization. This requires installing processes with mode 4755. Upon startup, processes will immediately decrease privileges to the lowest setting, and only increase privileges to set RT priority. The MagAOXApp will do this by default during construction.

5 TCS Interface

The instrument-TCS interface at Magellan is well documented in Eychaner (2015). Instruments connect via a TCP/IP socket and send and receive formatted ASCII. We have already implemented a process called the MagAOI (for MagAO Interface) which handles TCS queries at 1 Hz. This retrieves all data available from the TCS which is relevant to the MagAO system. We will adapt this code to work as an MagAOXApp (and INDI Driver and Client) and use it to manage interfacing with the TCS. In Appendix C we list all the TCS parameters which will be used.

As part of the MagAO test system, we developed a software TCS simulator which will be used for MagAO-X software development and lab testing.

6 User Interfaces

Based on long experience using it on the MagAO project, we plan to avoid running GUIs via x-forwarding on ssh. There are two main GUIs to be provided.

---

See VisAO_base() at https://visao.as.arizona.edu/software_files/visao/html/VisAOApp_base_8cpp_source.html for a working example
6.1 The Astronomer Interface: Following typical practice in CAAO INDI implementations (such as at the LBTI), astronomers will interact with the instrument using a GUI implemented using the jQuery UI framework, and running on a web browser. This will be served by a web server running on the AOC, connected to the AOC INDI server via the W3 fcgi protocol. This will be very flexible, allowing astronomers to use the workstations in the Clay control room with minimal fuss.

We will also provide support for observation scripting through this interface.

6.2 The AO Operator Interface: Experience on MagAO has shown that reliable high-speed display of AO status, including PWFS images and DM commands and positions, is extremely helpful in optimizing AO performance. To that end, we will implement a custom AO interface served on the AOC. It will make use of 4 monitors, and its organization will be optimized for ease of use. For instance, all buttons needed to operate the AO system will be located on a single pane – it will not be necessary to switch tabs or windows while operating the loop. Where appropriate, this may also make use of a web-browser interface (likely re-using code from the Astronomer Interface). Where needed, compiled Qt will be used for high performance.

To support the reliable high-speed AO updates, we will send telemetry and diagnostic data from the RTC and ICC to the AOC on an as-displayable basis. For instance, it is typically only possible to display PWFS images at \( \sim 30 \) Hz. In this case, a decimator process on the RTC will send frames on only 30 fps to the AOC. This will minimize network traffic, and processing time devoted to sending telemetry.

7 Real-time Software

For the real-time control of the AO loops we will use the RT software (RTS) developed by MagAO-X Co-PI Olivier Guyon for the Subaru SCExAO instrument. It is Linux-based, open-source C code along with high-level scripts. It uses publicly available libraries, including CUDA and MAGMA (for GPU computing), FFTW, FISIO, GSL. The source code is available at [https://github.com/oguyon/AdaptiveOpticsControl](https://github.com/oguyon/AdaptiveOpticsControl).

Because we are using essentially identical hardware to SCExAO (BMC 2k MEMS and OCAM-2K EMCCD) we save significant development time in implementing our RTS. Here we provide a very brief overview of the highlights of this system. More details are given in Appendix D to this Section.

7.1 Performance on Hardware: The RTS runs on a single multi-core computer. Minimum 15 cores system, 128GB ram (heavy use of shared memory and shielded processes running on single core). Supports NVIDIA hardware (CUDA lib). Interfaces to hardware through shared memory structure. Hardware already coupled with RTS: BMC deformable mirror, Ocam2k camera, SAPHIRA camera (with UH readout electronics), OwlCam InGaAs Raptor Photonics camera, Andor sCMOS.

7.2 Speed: Largely limited by hardware. Fully system timing stable at 10us level, and RTS latency due to IPC, TCP transfers between computers, and GPU transfers is \(< 100 \mu s\) total, so it can drive a \(\sim 10\) kHz loop on multi-computer system, and \(\sim 20\) kHz loop on single computer. SCExAO implementation drives 2000-actuator, 14,400-sensor loop at 3.5kHz, limited by camera readout speed.

7.3 Flexible architecture: All input, output and intermediate data is stored as shared memory. A common format for all shared memory data streams facilitates software development. Multiple processes run simultaneously to perform operations on shared memory streams. Additional processes can be deployed (for example, real-time
analysis of an intermediate data stream) without impacting existing processed.

IPC is built in the shared memory structure which contains POSIX semaphores (default of 10 semaphores, more if needed): 10 different processes can run on the same input. Each process waits on input stream(s), and posts output stream(s) semaphore(s), so real-time operations can be chained, with multiple branches.

References

Downey, E. C. 2007, 755, L28


### Coding Standards

Here we show some sketches of our standard coding practices, including use of doxygen comments.

```cpp
///Brief description for one parameter function
/** Long description
 * \returns functions should return 0 on success, and a negative integer to indicate error.
 * \tparam T document the type here.
 */
template<typename T>
int aFunction( T & param /**< \[in/out\] documentation for param*/ )
{
    //code goes here.
    return 0; ///\retval 0 on success.
}

///Brief description for two or more parameter function
/** Long description
 * \returns functions should return 0 on success, and a negative integer to indicate error.
 * \tparam T1 document the type of param1 here.
 * \tparam T2 document the type of param2 here.
 */
template<typename T1, typename T2>
int aFunction( T1 & param1 /**< \[out\] documentation for param1, an output T2 & param2 /**< \[in\] documentation for param2, an input */
{
    //code goes here.
    return 0; ///\retval 0 on success.
}

///Brief description for a class
/** Long description
 * \tparam _T document type _T/T here.
 */
template<typename _T>
class aClass
```
```cpp
{
public:
  typedef _T T; ///< public typedefs first, with documentation. All template
               // parameters typedef-ed as shown.

  aClass(); ///<Default c’tor

  ~aClass(); ///<Destructor.

protected:
  typeT member1; ///<Document protected members.

public:

  int actionFoo( T & inPlace /**< [in/out] parameter documentation*/ );

  int actionfoo( T & after, ///< [out] parameter documentation
                T & before /**< [in] parameter documentation
                  );

}
```
B Logging Code Sketch

Here we sketch the logging framework.

```cpp
namespace logger {

struct timespecX {
    int64_t time_s;
    int32_t time_ns;
};

//Logger events are declared:
struct loop_closed {
    const uint32_t eventCode = 1000;

    void length(uint32_t * logPtr /**< A pointer to a log entry, in this case not used *//* */) {
        return 12;
    }
};

struct tel_pos {
    const uint32_t eventCode = 103458;

    void length(uint32_t * logPtr /**< A pointer to a log entry, in this case not used */) {
        return 12 + 8 + 7 + 6 + 6 + 4 + 7; // the size of the TCS responses, after '.' is removed.
    }
};

struct user_log {
    const uint32_t eventCode = 38958;
};

//etc...

//And template specializations of the log function:
template<

```
void log();

/// Log specialization for the loop closed event
template<>
inline void log<loop_closed>()
{
    // Step 1: get timestamp
    // Step 2: format and store log
}

/// Log specialization for telescope position
template<>
inline void log<tel_pos>( char[8] telra, ///< Telescope RA as returned by TCS, with ‘.’ removed
                       char[7] teldc, ///< Telescope Dec as returned by TCS, with ‘.’ removed
                       char[6] telep, ///< Telescope Equinox as returned by TCS, with ‘.’ removed
                       char[6] telha, ///< Telescope HA as returned by TCS, with ‘.’ removed
                       char[4] telam, ///< Telescope Airmass as returned by TCS, with ‘.’ removed
                       char[7] rotangle ///< Telescope rotator angle as returned by TCS, with ‘.’ removed
                      )
{
    // Step 1: get timestamp
    // Step 2: format and store log
}

/// Log specialization for the loop closed event
template<>
inline void log<user_log>( const std::string & fromUser )
{
    // Step 1: get timestamp
    // Step 2: format and store log
    // Note: here the format must include a string length.
}

// etc...

}; // namespace logger

And then within the code itself entries such as

using namespace logger;
log< loop_closed >();

log< tel_pos > ( telra, teldc, telep, telha, telam, rotangle );

// User enters a log from a GUI:
// std::string fromUser <--- "Photometric conditions"

log< user_log > ( fromUser );
C TCS Parameters

Here we collect the various telescope and environment parameters which will be queried and logged.

<table>
<thead>
<tr>
<th>TCS Name</th>
<th>TCS Format</th>
<th>Stored As</th>
<th>State [Bytes]</th>
<th>Rate [Hz]</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>telescope</td>
<td>YYYY-MM-DD</td>
<td>char[8]</td>
<td>8</td>
<td>0.1</td>
<td>UT date in year month day format.</td>
</tr>
<tr>
<td>ra</td>
<td>HH:MM:SS:SS</td>
<td>char[8]</td>
<td>8</td>
<td>1</td>
<td>Right ascension in hours, minutes, and seconds.</td>
</tr>
<tr>
<td>dmostat</td>
<td>DD:DD:DD:DD</td>
<td>char[9]</td>
<td>3</td>
<td>1</td>
<td>Dome status (0 = closed, 1 = open, 2 = unknown)</td>
</tr>
<tr>
<td>teldguide</td>
<td>ab:ab:ab:ab</td>
<td>char[4]</td>
<td>4</td>
<td>1</td>
<td>Telescope and guider motion status (see below)</td>
</tr>
<tr>
<td>gdmounmv</td>
<td>abc:abc:abc</td>
<td>char[4]</td>
<td>4</td>
<td>1</td>
<td>Telescope and rotator motion status flags (see below)</td>
</tr>
<tr>
<td>telfocus</td>
<td>$FFFFF$</td>
<td>char[6]</td>
<td>6</td>
<td>1</td>
<td>Secondary mirror focus (Z axis) set (instrument) offset, in microns.</td>
</tr>
<tr>
<td>velocus</td>
<td>$FFFFF$</td>
<td>char[6]</td>
<td>6</td>
<td>1</td>
<td>Secondary mirror focus (Z axis) encoder reading, in microns.</td>
</tr>
<tr>
<td>telrlof</td>
<td>R</td>
<td>char[1]</td>
<td>1</td>
<td>0.1</td>
<td>Rotator of interest (0 to 3 are NASW, NASE, CASS, AUX1, AUX2, and AUX3 respectively).</td>
</tr>
<tr>
<td>rotnode</td>
<td>R</td>
<td>char[1]</td>
<td>1</td>
<td>0.1</td>
<td>Rotator tracking mode; normally either 0 (OFF; no tracking) or 2 (EQU; equatorial tracking, rotator tracks sky).</td>
</tr>
<tr>
<td>User Catalog Input</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>catrim</td>
<td>TTT:TTT:TTT</td>
<td>char[3]</td>
<td>3</td>
<td>0.1</td>
<td>Current catalog object rotator offset mode; one of OFF, EQU, GRV, or HRZ.</td>
</tr>
<tr>
<td>catobj</td>
<td>string</td>
<td>char[30]</td>
<td>30</td>
<td>0.1</td>
<td>Current catalog object name (up to 30 characters, containing no spaces).</td>
</tr>
<tr>
<td>Environment</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>twdimw</td>
<td>$FFFF$</td>
<td>char[4]</td>
<td>4</td>
<td>0.1</td>
<td>30-second average FWHM value from the active guider.</td>
</tr>
<tr>
<td>dimemw</td>
<td>$FFFF$</td>
<td>char[4]</td>
<td>4</td>
<td>0.1</td>
<td>SDIM seeing, available from wx database, not TCS.</td>
</tr>
<tr>
<td>magfw</td>
<td>$FFFF$</td>
<td>char[4]</td>
<td>4</td>
<td>0.1</td>
<td>Baade seeing, available from wx database, not TCS.</td>
</tr>
<tr>
<td>wxtemp</td>
<td>TTT:TTT:TTT</td>
<td>char[3]</td>
<td>5</td>
<td>0.1</td>
<td>Outside temperature (degrees Celsius).</td>
</tr>
<tr>
<td>wxpsres</td>
<td>PPPPPP:PPP</td>
<td>char[9]</td>
<td>3</td>
<td>0.1</td>
<td>Outside pressure (millibars).</td>
</tr>
<tr>
<td>wxhumid</td>
<td>HHH:HH:HH</td>
<td>char[3]</td>
<td>5</td>
<td>0.1</td>
<td>Outside humidity (percent).</td>
</tr>
<tr>
<td>wxwindr</td>
<td>DDD:DD:DD</td>
<td>char[3]</td>
<td>5</td>
<td>0.1</td>
<td>Outside wind direction (degrees).</td>
</tr>
<tr>
<td>telc</td>
<td>TTT:TTT:TTT</td>
<td>char[3]</td>
<td>5</td>
<td>0.1</td>
<td>Mirror cell temperature (degrees Celsius).</td>
</tr>
<tr>
<td>tescell</td>
<td>TTT:TTT:TTT</td>
<td>char[3]</td>
<td>5</td>
<td>0.1</td>
<td>Secondary mirror cell temperature, skyward side (degrees Celsius).</td>
</tr>
<tr>
<td>taire</td>
<td>TTT:TTT:TTT</td>
<td>char[3]</td>
<td>5</td>
<td>0.1</td>
<td>Primary mirror air temperature (degrees Celsius).</td>
</tr>
</tbody>
</table>
3.3 Software
Appendix D

SCExAO Real-Time Architecture
AO Loop Control Software

Overview

Linux-based

Open-source, no closed library

C code (~100k lines) + high-level scripts (baseline control interface using bash scripts provided)

Uses libraries: CUDA & MAGMA (GPU computing, optional), FFTW, FITSIO, GNU scientific library, readline

Source code + example simulated AO system:
https://github.com/oguyon/AdaptiveOpticsControl
Hardware

Hardware Requirements / compatibility:

RTS runs on a single multi-core computer. Minimum ~15 cores system, 128GB ram (heavy use of shared memory and shielded processes running on single core)

CPU only or CPU+GPU computing engine. Requires GPU(s) for high speed / high actuator count. Supports NVIDIA hardware (CUDA lib).

Can span multiple computers (for example, camera or DM driven by computer other than main RTS). Software uses and configures fast private low-latency TCP link (eg. 10GbE or 40GbE fibers) for transfers.

Interfaces to hardware through shared memory structure. Hardware already coupled with RTS: BMC deformable mirror, Ocam2k camera, SAPHIRA camera (with UH readout electronics), OwlCam InGaAs Raptor Photonics camera, Andor sCMOS.
Capabilities

Speed

Largely limited by hardware. Fully system timing stable at 10us level, and RTS latency due to IPC, TCP transfers between computers, and GPU transfers is <100us total → can drive ~10 kHz loop on multi-computer system, and ~20 kHz loop on single computer. SCExAO implementation drives 2000-actuator, 14,400-sensor loop at 3.5kHz, limited by camera readout speed.

Flexible architecture

All input, output and intermediate data is stored as shared memory. A common format for all shared memory data streams facilitates software development. Multiple processes run simultaneously to perform operations on shared memory streams. Additional processes can be deployed (for example, real-time analysis of an intermediate data stream) without impacting existing processed.

IPC is built in the shared memory structure which contains POSIX semaphores (default of 10 semaphores, more if needed): 10 different processes can run on the same input. Each process waits on input stream(s), and posts output stream(s) semaphore(s) → Real-time operations can be chained, with multiple branches
Example control GUI (bash scripts)
For each file:
conf_<name>_name.txt points to archived file location
conf/conf_<name>_name.txt are read by function ReadConfFile for
loading into shared memory and FITS copy to
./conf/aol#_<name>.fits

Conventions :
**Modal DM**: “actuators” indices have no spatial meaning
  → No spatial filtering options
  → “Direct write” CM and “Modal” CM are the same (1 mode = 1 actuator)
**Zonal DM**: actuator indices correspond to spatial coordinates
  → Need linear transformation between mode coefficients and actuators

**If re-using masks, keep from previous calibration**
Control Matrix Computation Modes

**WFSnorm**  
(.conf/WFSnormalize.txt)  
**WFS normalization mode**  
C code: AOconf[loop].WFSnormalize

- 0: Do not normalize WFS images
- 1: Normalize WFS images

WFSnorm should be left unchanged between RM acquisition and Loop control

**DMprimaryWrite**  
(.conf/DMprimWriteON.txt)  
**DM primary write**  
C code: DMprimaryWrite_ON

- 0: DM primary write is off
- 1: DM primary write is on

**CMmode**  
(.conf/CMmode.txt)  
**Combined Control matrix mode**  
C code: MATRIX_COMPUTATION_MODE

- 0: not combined: control matrix is WFS pixels → modes
  → Linking aol#_DMmode_meas ← aol#_modeval
  → modesextractwfs reads from aol_DMmode_meas instead of computing
- 1: combined: control matrix is WFS pixels → DM actuators

**DMMODE**  
(.conf/DMMODE.txt)  
**DM mode (zonal vs. modal)**  
Bash script only, only affects bash scripts and options

- ZONAL: pixel coordinates correspond to DM actuators physical location
  → spatial filtering enabled for DM modes creation
  → blocks built by spatial frequencies, user can set independent gain values for mode blocks
- MODAL: DM pixels correspond to abstract modes
  → no spatial filtering, setting 1 block only

Note: **DMMODE**=ZONAL → **CMMODE**=MODAL (CPA-splitting of modes into blocks)

**GPUmode**  
(.conf/GPUmode.txt)  
**# of GPUs to use for CM multiplication**  
C code: AOconf[loop].GPU

- 0: use CPU
- >0: number of GPUs

if CMmode=1 and GPUmode>0:

**GPUallmode**  
(.conf/GPUall.txt)  
**Use GPU for all computations**  
C code: AOconf[loop].GPUall = COMPUTE_GPU_SCALING

- 0: Use CPU for WFS reference subtraction and normalization
  → WFS reference subtraction and normalization done by CPU (imWFS0→ imWFS1→ imWFS2)
  → CM multiplication input is imWFS2 (GPU or CPU)
- 1: Use GPU for all computation
  → WFS reference subtraction and normalization done by GPU
  → GPU-based CM multiplication input is imWFS0
## Control Matrices

<table>
<thead>
<tr>
<th>Matrix</th>
<th>Description</th>
<th>Input → output</th>
<th>Gain control (primary write)</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>contrM</td>
<td>Full modal control matrix</td>
<td>WFSpix → DMmodes</td>
<td>0.0&lt;loopgain&lt;1.0 0.0&lt;DMmodes_GAIN[m]&lt;1.0</td>
<td>gainMB has no effect and will not update contrM</td>
</tr>
<tr>
<td>contrMc</td>
<td>Full combined control matrix</td>
<td>WFSpix → DMactuators</td>
<td>0.0&lt;gainMB[k]&lt;1.0 0.0&lt;loopgain&lt;1.0</td>
<td>contrMc re-built for each change of gainMB If DM is MODAL: gainMB has no effect and will not update contrM</td>
</tr>
<tr>
<td>contrMcact</td>
<td>Combined control matrix, only active pixels</td>
<td>Active WFS pixels → Active DM actuators</td>
<td>0.0&lt;gainMB[k]&lt;1.0 0.0&lt;loopgain&lt;1.0</td>
<td>contrMcact re-built for each change of gainMB If DM is MODAL: gainMB has no effect and will not update contrM</td>
</tr>
</tbody>
</table>

### Control Matrix operation(s)

<table>
<thead>
<tr>
<th>CMmode</th>
<th>GPUmode</th>
<th>GPUallmode</th>
<th>Camera read output (Read_cam_frame)</th>
<th>WFS reference subtraction</th>
<th>Control Matrix operation(s)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0</td>
<td>0</td>
<td>→ imWFS1</td>
<td>CPU subtraction → imWFS2</td>
<td>contrM x imWFS2 → DMmode_meas [CPU] DMmode_meas → cmd_modes [CPU] DMmodes x cmd_modes → dmC [CPU]</td>
</tr>
<tr>
<td>0</td>
<td>&gt;0</td>
<td>0</td>
<td>→ imWFS1</td>
<td>CPU subtraction → imWFS2</td>
<td>contrM x imWFS2 → DMmode_meas [GPU] DMmode_meas → cmd_modes [CPU] DMmodes x cmd_modes → dmC [GPU]</td>
</tr>
<tr>
<td>0 [to be done]</td>
<td>&gt;0</td>
<td>1</td>
<td>→ imWFS0 / GPU_alpha, GPU_beta</td>
<td>done in GPU as part as CM mult</td>
<td>contrM x imWFS0 → DMmode_meas [GPU] DMmode_meas → cmd_modes [CPU] DMmodes x cmd_modes → dmC [GPU]</td>
</tr>
<tr>
<td>1</td>
<td>0</td>
<td>0</td>
<td>→ imWFS1</td>
<td>CPU subtraction → imWFS2</td>
<td>contrMc x imWFS2 → meas_act [CPU] meas_act → dmC [CPU]</td>
</tr>
<tr>
<td>1</td>
<td>&gt;0</td>
<td>0</td>
<td>→ imWFS1</td>
<td>CPU subtraction → imWFS2</td>
<td>contrMcact x imWFS2_active → meas_act_active [GPU] meas_act → dmC [GPU]</td>
</tr>
<tr>
<td>1</td>
<td>&gt;0</td>
<td>1</td>
<td>→ imWFS0 / GPU_alpha, GPU_beta</td>
<td>done in GPU as part as CM mult</td>
<td>contrMcact x imWFS0_active → meas_act_active [GPU] meas_act → dmC [CPU]</td>
</tr>
</tbody>
</table>

![Control Matrix Diagram](image-url)
Auxillary processes

Decompose WFS measurements in modes

Decompose DM commands in modes + apply modal mult gains
Zonal response matrix acquisition → masks

`.auxscripts/aolMeasureZrespmat`

`.zrespmtmp/zrespmat_pos.NNN.fits` (negative pokes)
`.zrespmtmp/zrespmat_neg.NNN.fits` (positive pokes)
`.zrespmtmp/wfsref0_NNN.fits` (WFS reference - accumulates)
`.zrespmtmp/wfsimRMS.fits` (WFS image RMS - accumulates)

`.auxscripts/aolCleanZrespmat`

*aolcleanzrm*

zrespmat.fits
dmmmap.fits
wfsmap.fits

 placeholders <name>/.<name>_$datestr.fits
placeholders ./conf/conf_<name>_name.txt
placeholders ./conf_zrm_staged/

placeholders ./<name>/.<name>_$datestr.fits
placeholders ./conf/conf_<name>_name.txt
placeholders ./conf_zrm_staged/

function_zresp_on → function_zresp_off

`.auxscripts/aolmkMasks`

*aolRMmkmasks*

wfsmask.fits
dmmaskRM.fits

dmslaved.fits
dmmask.fits

placeholders ./conf/conf_DMmaskRMp0.txt: DM mask RM: low level percentile (p0)
placeholders ./conf/conf_DMmaskRMc0.txt: DM mask RM: low level coefficient (c0)
placeholders ./conf/conf_DMmaskRMp1.txt: DM mask RM: high level percentile (p1)
placeholders ./conf/conf_DMmaskRMc1.txt: DM mask RM: high level coefficient (c1)

placeholders ./conf/conf_WFSmaskRMp0.txt: WFS mask RM: low level percentile (p0)
placeholders ./conf/conf_WFSmaskRMc0.txt: WFS mask RM: low level coefficient (c0)
placeholders ./conf/conf_WFSmaskRMp1.txt: WFS mask RM: high level percentile (p1)
placeholders ./conf/conf_WFSmaskRMc1.txt: WFS mask RM: high level coefficient (c1)

placeholders ./conf/conf_WFSmaskSNRr.txt: fraction of elements rejected due to low SNR
Making control modes (Zonal DM)

Create or load dmmask active DM actuators

Create or load dmslaved slaved DM actuators

Create or load CPAmodes Fourier modes

Create or load emodes Excluded modes

Create or load zrespM zonal RM

Create or load RMMmodes low order modes

Create or load RMMresp low order response

Create or load wfsmask active DM actuators

CREATE DM MODE BLOCKS
Modes are DM-orthogonal within and between blocks

ORTHOGONALIZE MODES IN WFS SPACE

create or load fmodes0all
remove WFS null space within each block [SVDlim00]
remove null space within each block [SVDlim01]
remove DM modes contained in previous blocks, and enforce DM-space orthogonality between blocks [rmslim0]
remove WFS modes contained in previous blocks, and enforce WFS-space orthogonality between blocks [rmslim1]
remove null space within each block [SVDlim]
OFFSETTING
LOWFS (loop #1, dm01) → PyWFS (loop #0, dm00)

Green color: process is part of loop #1

DM channels, loop 0

00 OFFSET (flat)
01 TT LQG
02 PyWFS RM
03 PyWFS control

04 ZAP
05 LOWFS
06 speckle probes
07 speckle control

08 Zernike offsets
09 Astrom grid
10 Turbulence
11 AO sim

Loop 0 DM modes

dm01disp

Loop 0 WFS modes

dmwref0

DM channels, loop 0

00 OFFSET (flat)
01 TT LQG
02 PyWFS RM
03 PyWFS control

04 ZAP
05 LOWFS
06 speckle probes
07 speckle control

08 Zernike offsets
09 Astrom grid
10 Turbulence
11 AO sim

Loop 0 DM modes

dm01disp

Loop 0 WFS modes

dmwref0

CPU (part of dmcomb)

GPU modal WFS offset [GPUdm2wfsrefM_dm#]

CPU (part of dmcomb)

GPU zonal WFS offset [GPUdm2wfsrefZ_dm#]

CPU zonal WFS offset [aol0zploop#]
Processes, output to DM (main loop)

1. **Main process** 
   - [aol#run] in aol#RT script auxscripts/aolrun
   - CPU (+ GPU)

2. **WFS image**
   - dark subtract

3. **if modal**
   - Direct DM Write → actuators
     - if DMprimaryWrite_ON

4. **DM Primary Write**
   - complatency_frame (measured by aolMeasureTiming)

5. **DM map (test)**
   - script aolPFcoeffs2dmmap
   - GPU or CPU

6. **DM filtering writeback**
   - [aol#dmfwb] in aol#RT auxscripts/aolPFcoeffs2dmmap
   - GPU or CPU

7. **WFS modes pixels → modes**
   - Extract WFS modes [aol#mexwfs] in aol#RT1 auxscripts/modesextractwfs
   - GPU or CPU

8. **Open loop mode coefficients**
   - [aol#_modeval ol]

9. **Predictive filter block**
   - input watch [aol#PFb0watchin]

10. **Predictive filter compute**
    - [aol#PFb0comp]

11. **Predictive filter engine**
    - [aol#PFb0apply] in aol0RT1

12. **Predicted mode coefficients**
    - [aol#_modevalPF]

13. **Current modal DM correction**
    - [aol#_modeval_dm_now]

14. **Modal filtering**
    - (clipping)
    - [aol#DMmode_GAIN]

15. **Current modal DM correction, filtered**
    - [aol#_modeval_dm_now_filt]

16. **DM map (test)**
    - [aol#dmfwb] in aol0RT auxscripts/aolmcoeffs2dmmap
    - GPU or CPU

17. **Status index**
    - 01
    - status index

18. **StatusM index**
    - 00
    - statusM index

19. **Timer index**
    - 00
    - timer index

20. **Semaphores**
    - sem3
    - sem2
    - sem4

21. **Note:** DM map & coefficients show correction applied
    - open loop = WFS residual – dm
    - Wfresidual = Open loop WF + dm
    - dm = Wfresidual – open loop

**Zonal DM only**

**Modal DM only**

**Processes, output to DM (main loop)**

- **Extract WFS modes** [aol#mexwfs] in aol#RT1 auxscripts/modesextractwfs
- GPU or CPU

- **Open loop mode coefficients** [aol#_modeval_ol]

- **Predictive filter block** input watch [aol#PFb0watchin]

- **Predictive filter compute** [aol#PFb0comp]

- **Predictive filter engine** [aol#PFb0apply] in aol0RT1

- **Predicted mode coefficients** [aol#_modevalPF]

- **Current modal DM correction** [aol#_modeval_dm_now]

- **Modal filtering** (clipping) [aol#DMmode_GAIN]

- **Current modal DM correction, filtered** [aol#_modeval_dm_now_filt]

- **DM map (test)** script aolPFcoeffs2dmmap

- **DM filtering writeback** [aol#dmfwb] in aol#RT auxscripts/aolPFcoeffs2dmmap

- **GPU or CPU**

- **Predictive filter block** buffer for predictive block [aol#_modevalol_logbuff0]

- **Predictive filter block** buffer for predictive block [aol#_modevalol_logbuff1]
Hardware Latency

Definition:
Time offset between **DM command issued**, and **mid-point between 2 consecutive WFS frames with largest difference**

Hardware Latency = DM soft + DM elec + DM phys + CAM readout/transfer + CAM processing + ½ exposure time

SCExAO:
- 2kHz loop: 1057 us
- 3kHz loop: 975 us
- difference = 82 us
- expected difference = 83us
→ 1us error
RM acquisition runs in CPU set aol#RT2

Loop:
Wait on and read WFS frame → allocate WFS frame to appropriate frame block
If poke required: wait RMdelay1, then poke